

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

HYUN SOO KIM, ET AL.

Application No.:

Filed:

For: LOGIC ELEMENT EMPLOYING SATURABLE ABSORBER

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

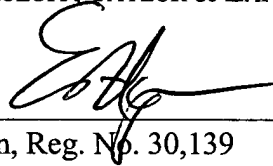
Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: _____

10/9/03



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PATENT
ATTORNEY DOCKET NO.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :
Serial No.:
Filed :
Title : optical logic element

Commissioner of Patents and Trademarks
Washington DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

Japan Patent Application No. JP 4- 75749 is directed to an optical logic element is disclosed which performs an XOR operation through utilization of the high-speed property of light. On each branched waveguide of a Mach-Zehnder interference type optical waveguide there is provided a phase modulating element whose refractive index undergoes a change when it is irradiated by light. The interference type optical waveguide is adapted to provide different optical output levels when the refractive index changes of the phase modulating elements are both zero or a predetermined value and when they differ from each other. Thus, the optical logic element is capable of performing the XOR or XNOR operation at an ultrahigh speed.

Respectfully submitted,

Date : _____

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

[illegible]

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450